

Ferroelectric measurement

Yuewei Yin

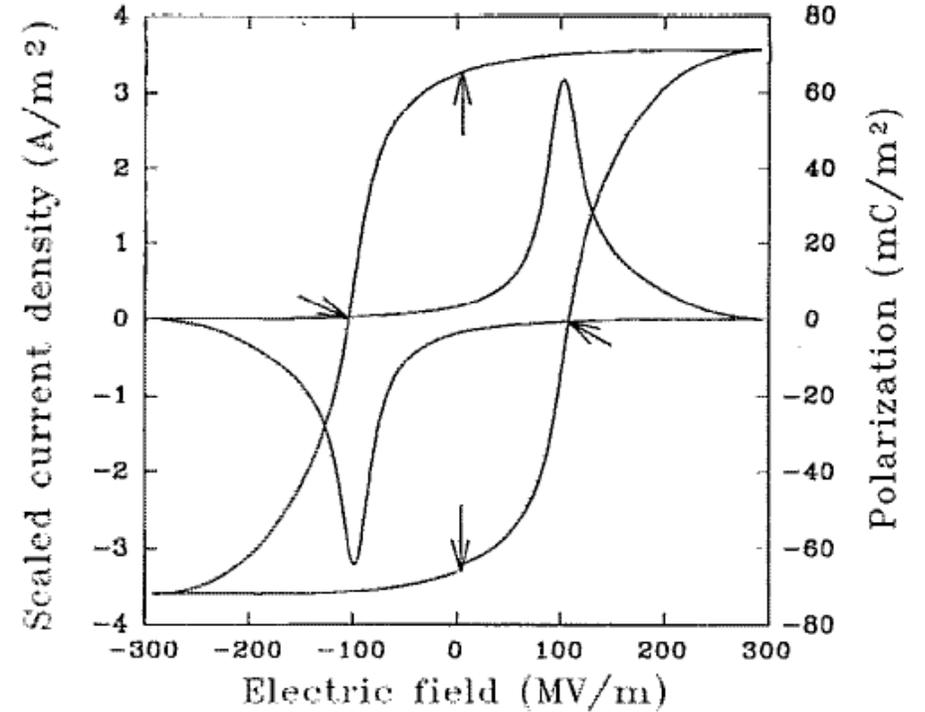
2016/01/29

Prof. Xu's Group meeting

Direct I-V measurement

$$i = i_{cap} + i_p + i_{cond} = C_s \frac{dV_s}{dt} + A_s \frac{dP_s}{dt} + \frac{V_s}{R_s}$$

There are three instantaneous current components in the series circuit, polarization current (i_p), conductive current (i_{cond} , if sample is lossy) and current due to stray linear capacitances (i_{cap}).

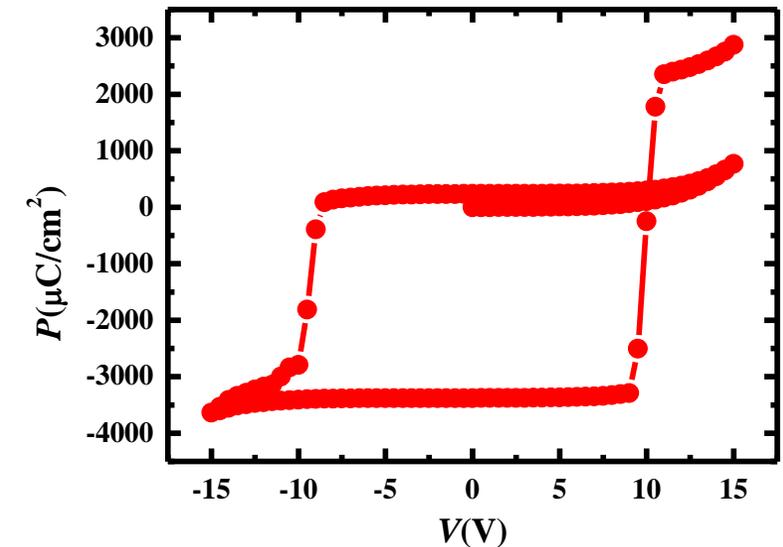
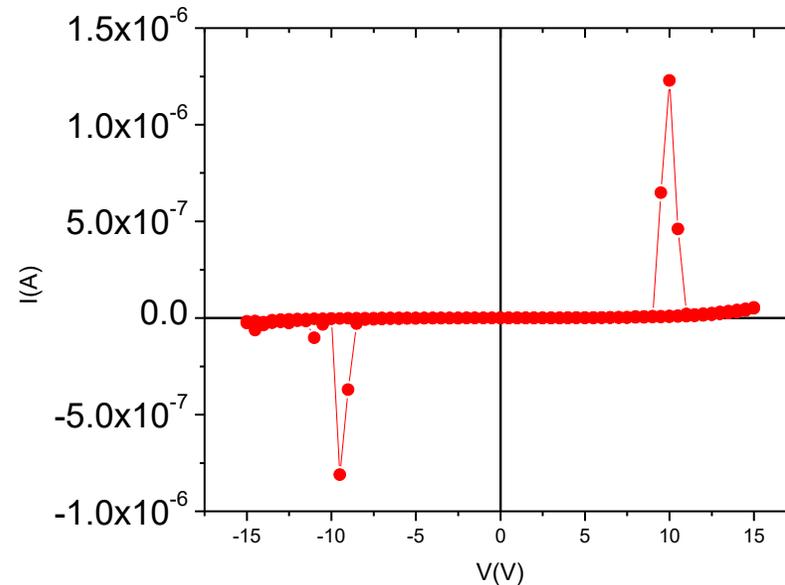


$$Q = \int I dt = \int 24I dV$$

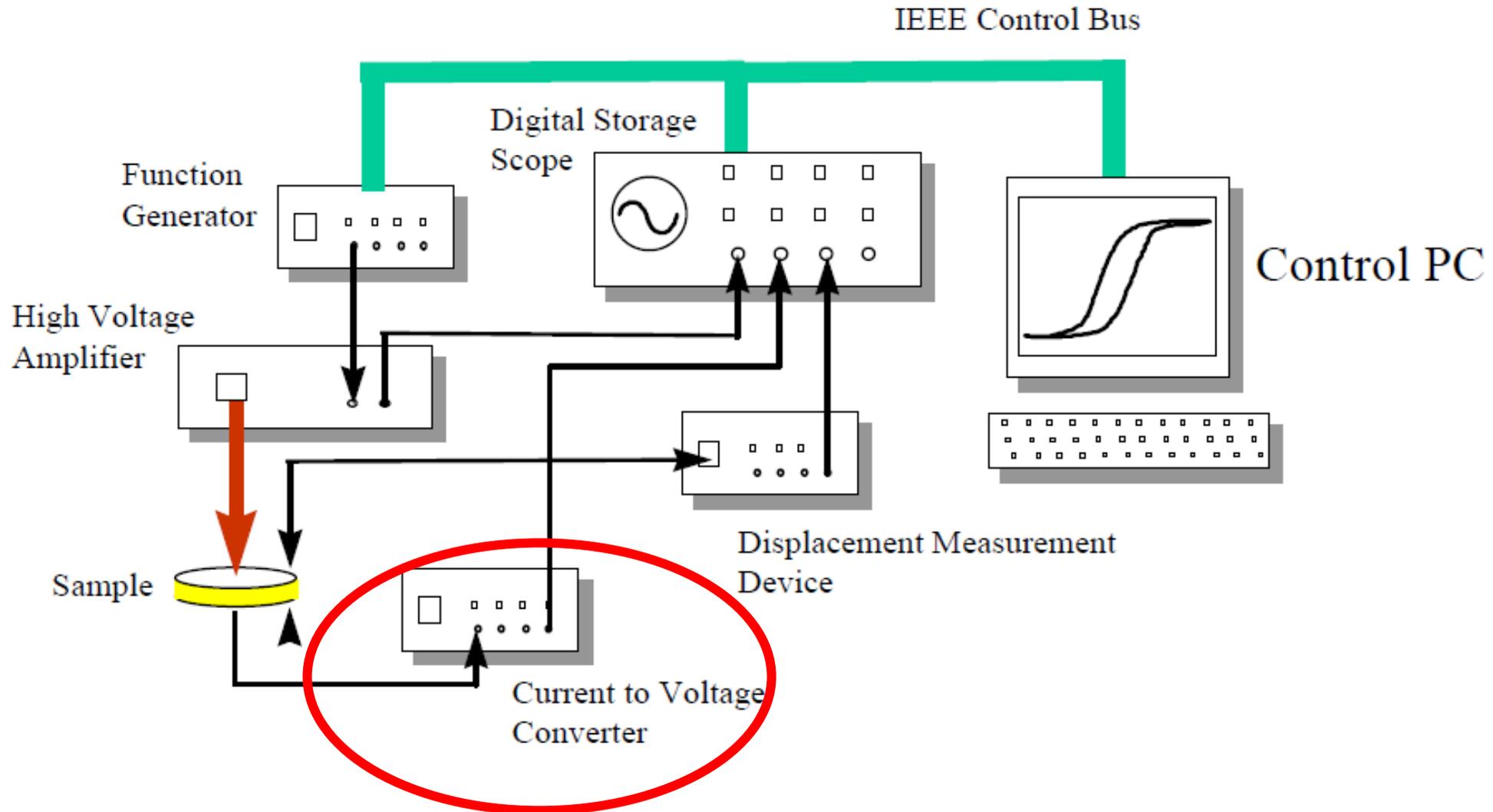
$$\frac{dV}{dt} = 15V/6min \quad dt = 24dV(sec/Volt)$$

$$P = Q/A \quad A \sim 0.005cm^2$$

The P is too large, cannot be real.

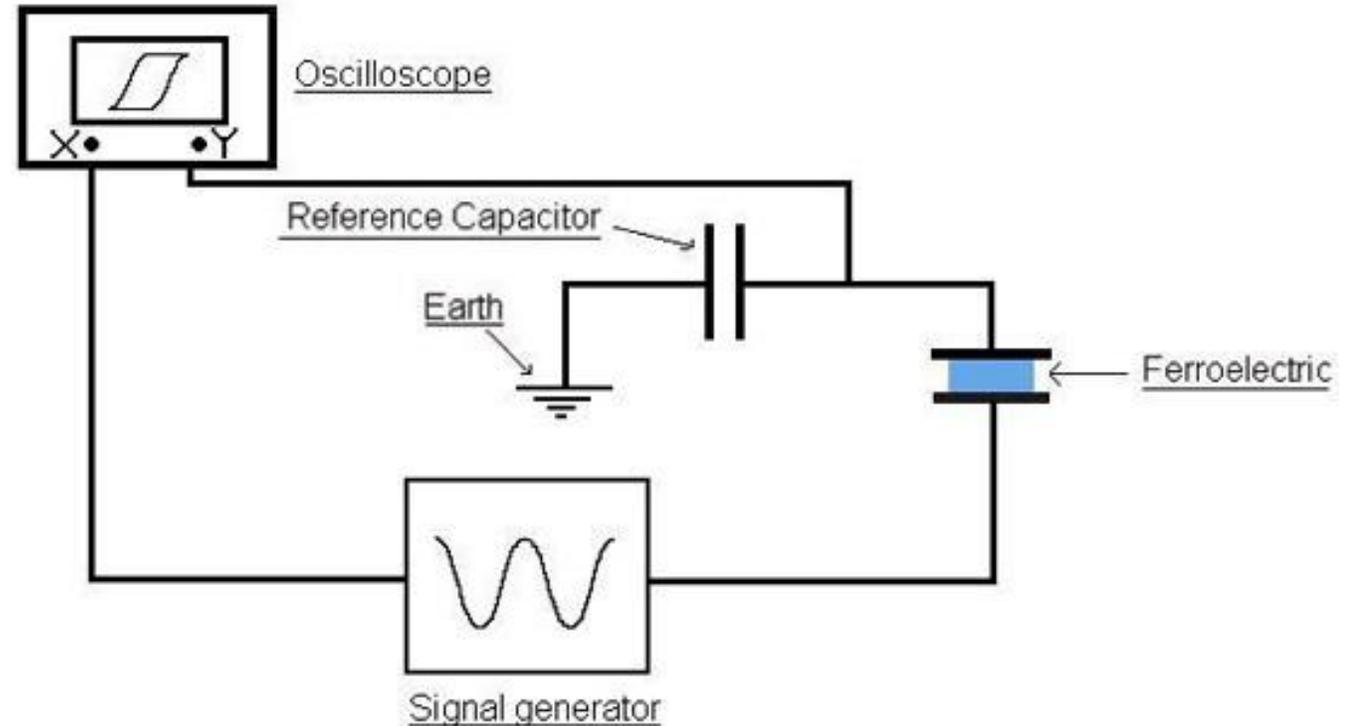


Ferroelectric measurement setup

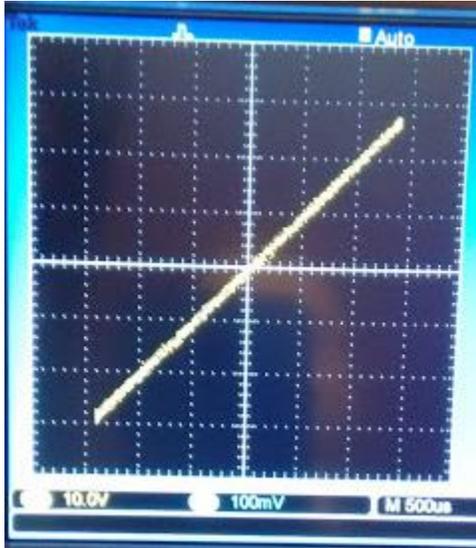


Sawyer-Tower (ST) circuit measurement

The circuit consists of two capacitors, one due to sample (C_x) and other one is a linear-known-valued sense-capacitor (C_{sense}). They are in series, where C_{sense} is chosen much greater than C_x so that voltage drop across C_{sense} is much less than that across C_x (sample). So the drive voltage V_d is almost equal to voltage across C_x . The voltage across C_{sense} , which gives polarization of the sample, is applied to vertical plates of the oscilloscope and the drive voltage after safe attenuation is applied to horizontal plates of the oscilloscope to measure electric field across the sample.



Lossy FE capacitor with leaky resistance and linear capacitance



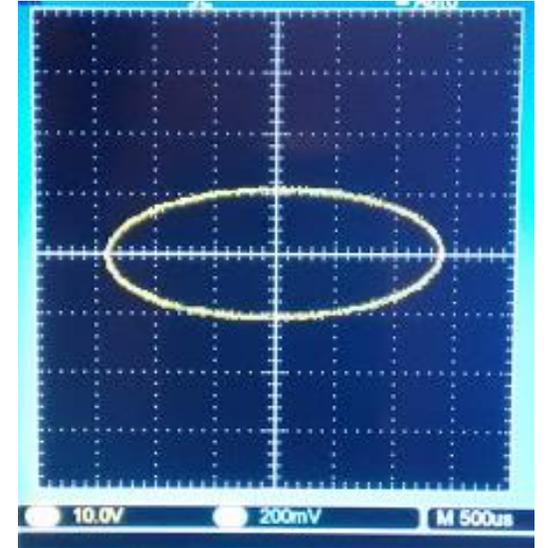
Ideal linear capacitor
0.1nF



Lossy capacitor
0.1 nF+2M Ω



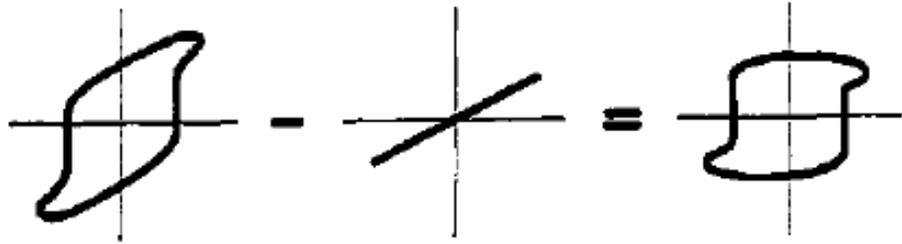
More lossy capacitor
0.1 nF+100 k Ω



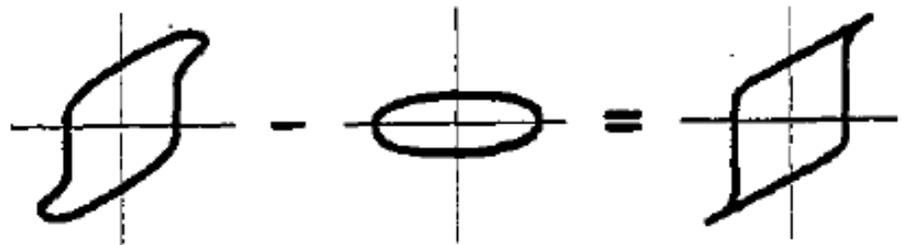
Ideal resistor
2M Ω

$$C_{\text{sense}} = 10\text{nF}$$

Compensate for the linear capacitance and lossy resistance



The linear capacitance can be compensated for by subtracting from the loop a straight line with the correct slope.

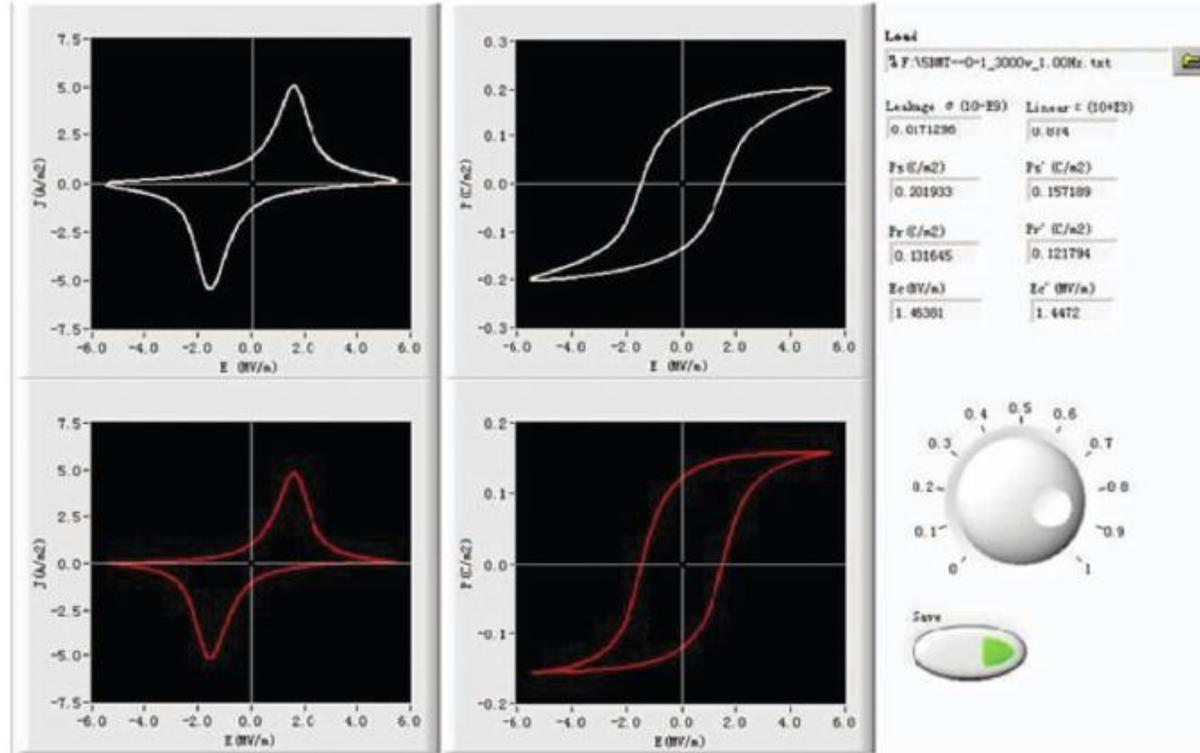
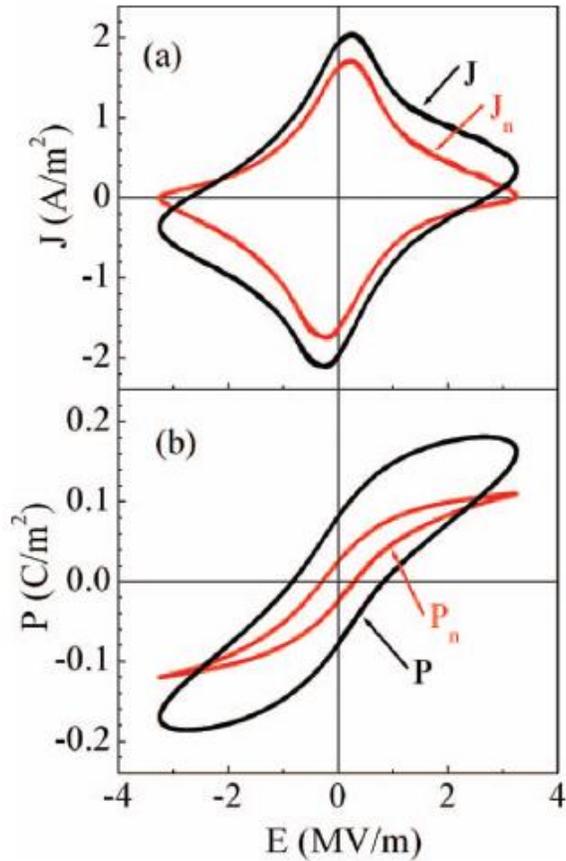


The losses can be compensated for by subtracting from the loop an ellipse of proper shape and **size** with its major **axis** along the E axis.



In the general, both losses and linear capacitance are compensated by producing an ellipse whose major **axis can** have any desired inclination with respect to the E axis and with any desired ratio **a : b** of the major to minor axis .

Computational compensate



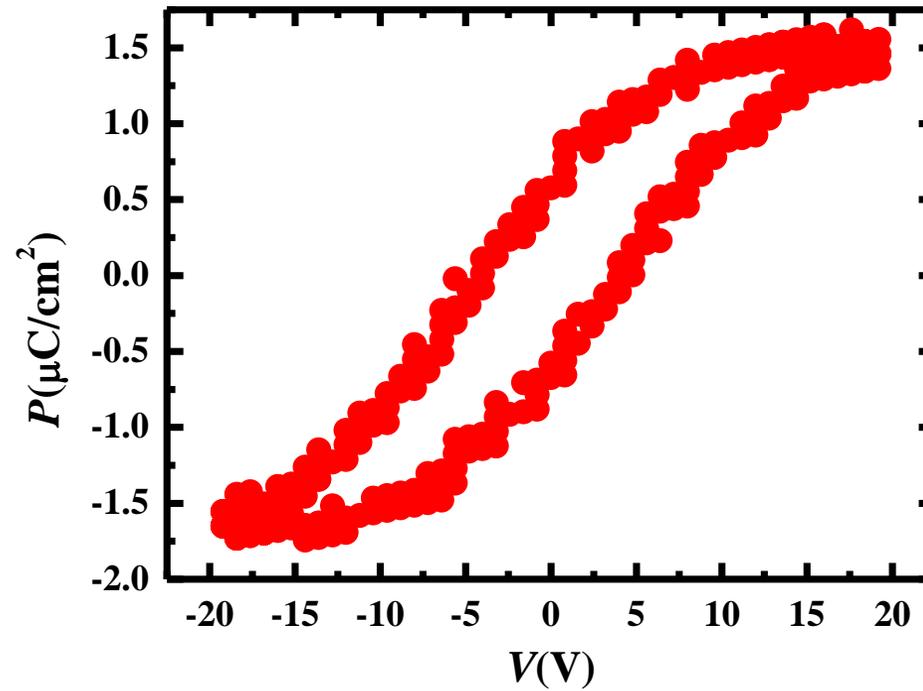
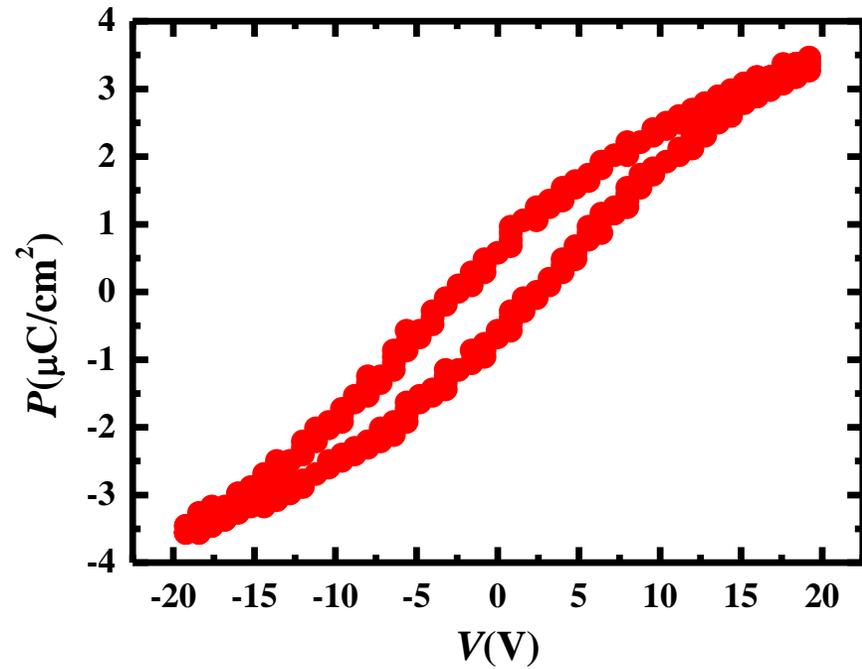
$$J = J_p + J_{cap} + J_{cond}$$

$$E = E_m \sin(\omega t)$$

$$J_{cap} = \epsilon_0 \epsilon \omega E_m \cos(\omega t)$$

$$J_{cond} = \sigma E_m \sin(\omega t)$$

Ferroelectric loop for BTO-3



Compensate loss resistor-1965

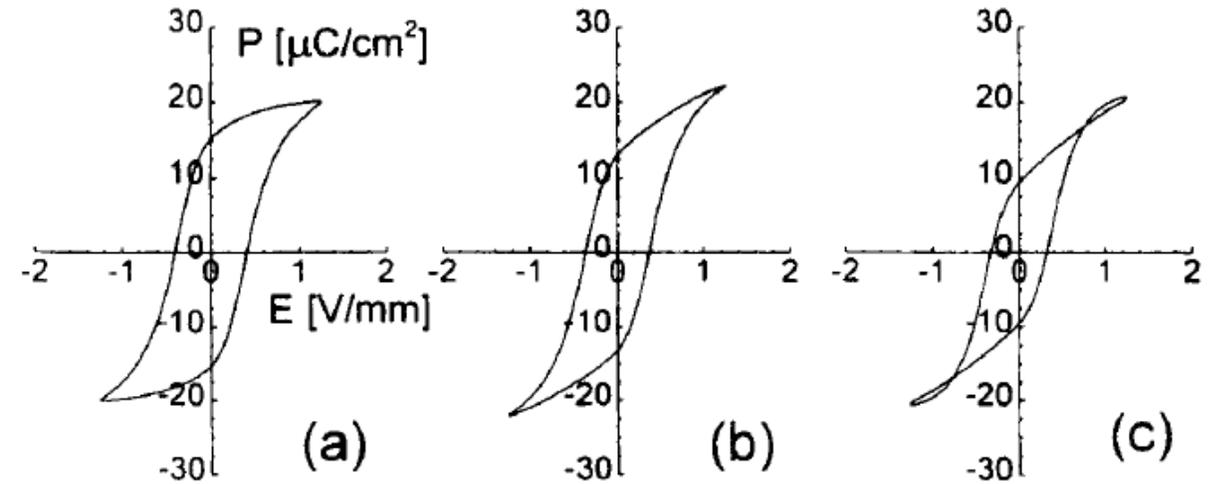
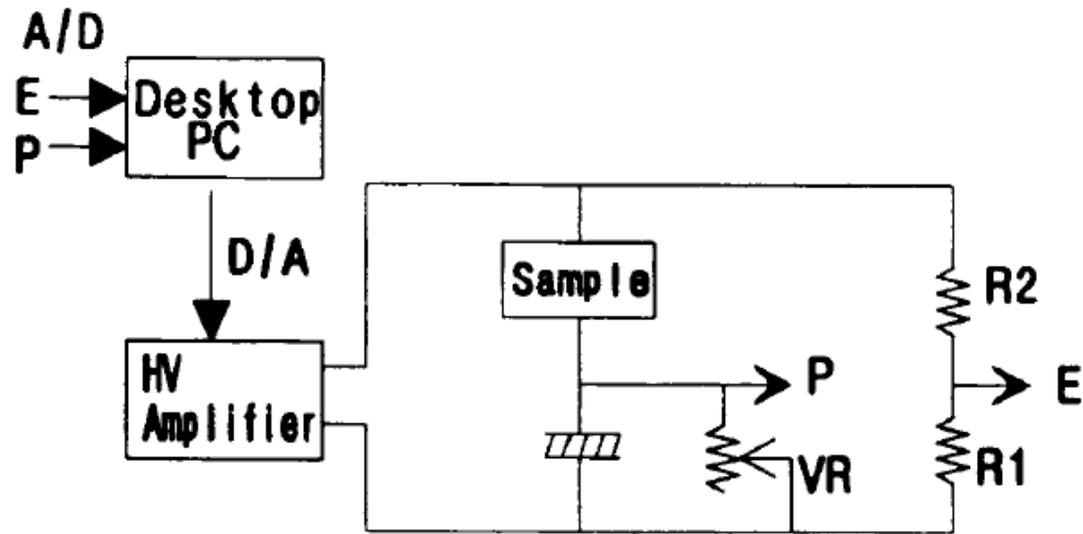


Figure 4 P-E plots of the PZT at the electric-field of 1.5KV/mm with variation of the magnitude of VR. (a) VR, high. (b) VR, properly adjusted. (c) VR, low.

$$C_s R_s = C_{\text{sense}} R_{\text{sense}}$$

Bridge circuit-1957

Adjusting R_c to equal the sample resistivity R_s removes the rounded ends of the displayed hysteresis loop. The residual capacitance C_r can be compensated and measured by adjusting C_c to produce a hysteresis loop ending in horizontal lines.

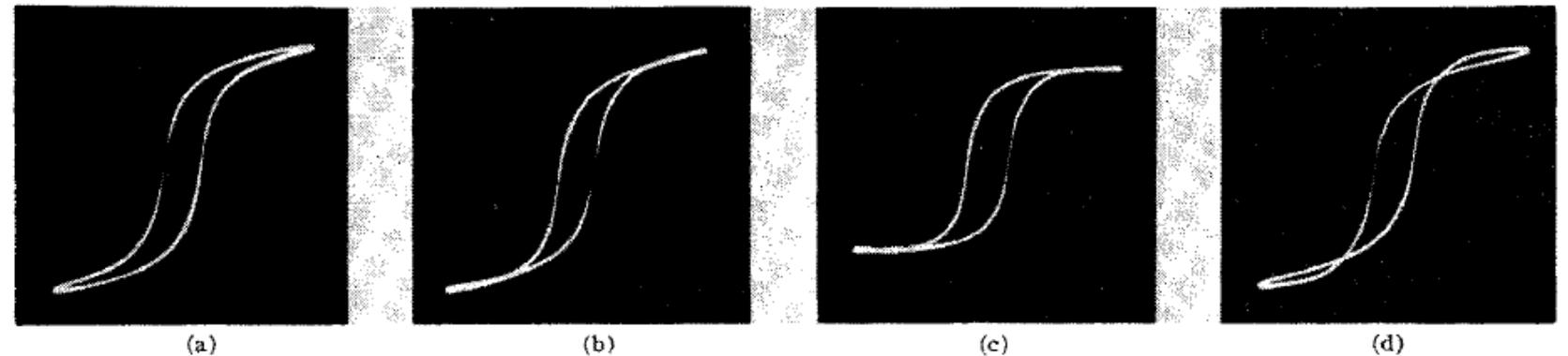
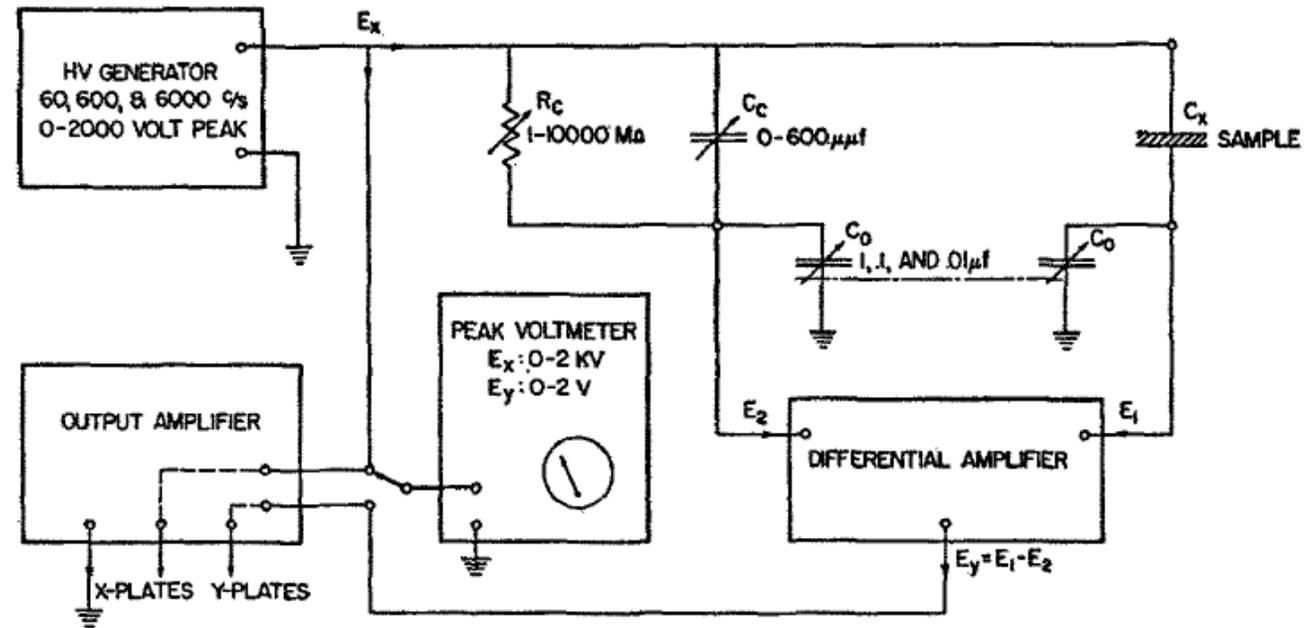
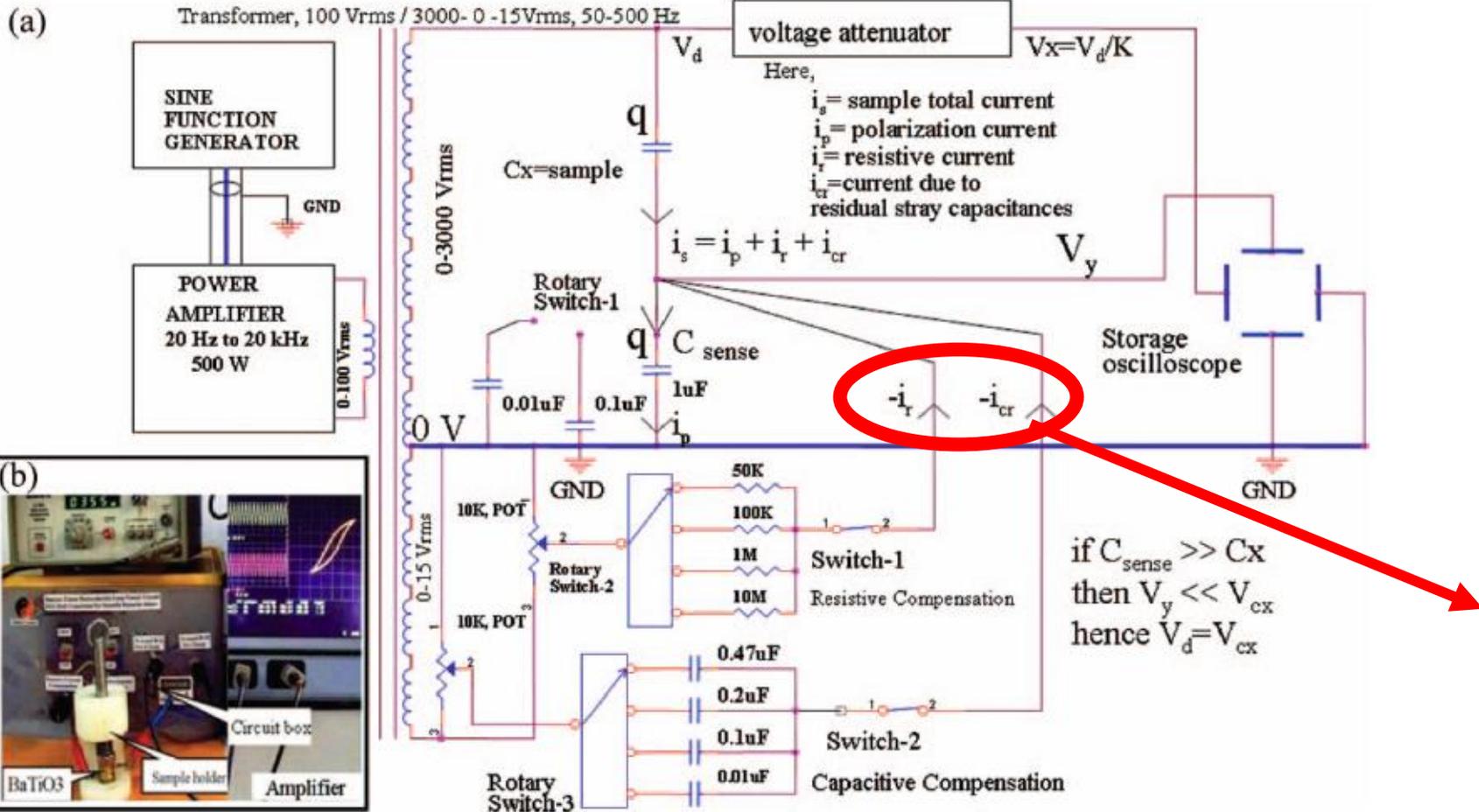


FIG. 11. Rochelle salt with artificial conductivity ($\rho = 5.8 \times 10^8 \Omega \text{ cm}$); hysteresis loops as measured by hysteresis bridge. $V_0 = 500 \text{ v}$, 60 cps; $A = 1.05 \text{ cm}^2$, $b = 0.179 \text{ cm}$, $Q_s = 0.22 \mu\text{C}$, $t = 0^\circ\text{C}$. (a) $R_c = \infty$, $C_c = 0$; (b) $R_c = 10^8 \Omega$, $C_c = 0$; (c) $R_c = 10^8 \Omega$, $C_c = 130 \mu\mu\text{f}$; (d) $R_c = 5 \times 10^7 \Omega$, $C_c = 0$.

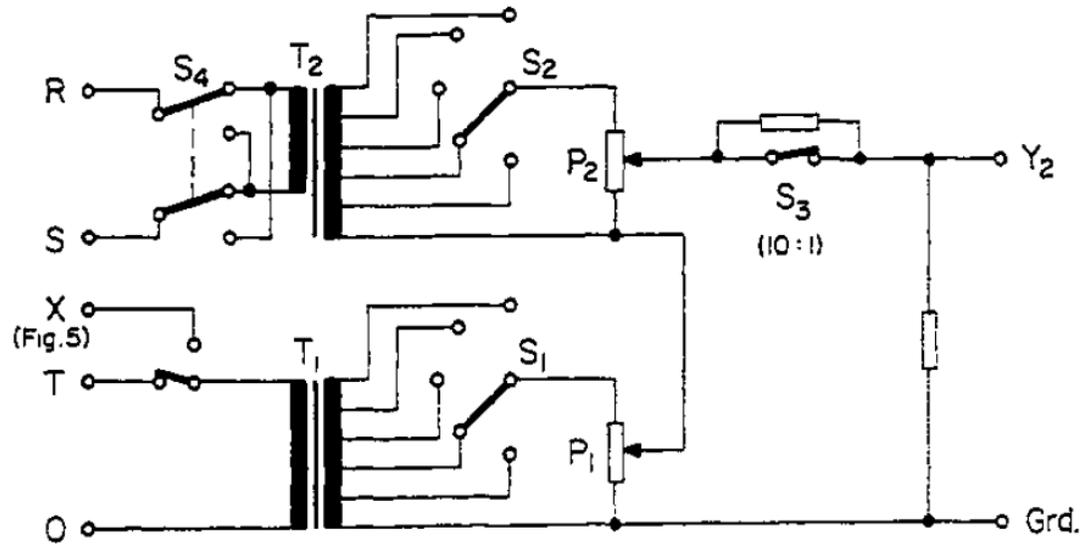
Negative current compensate



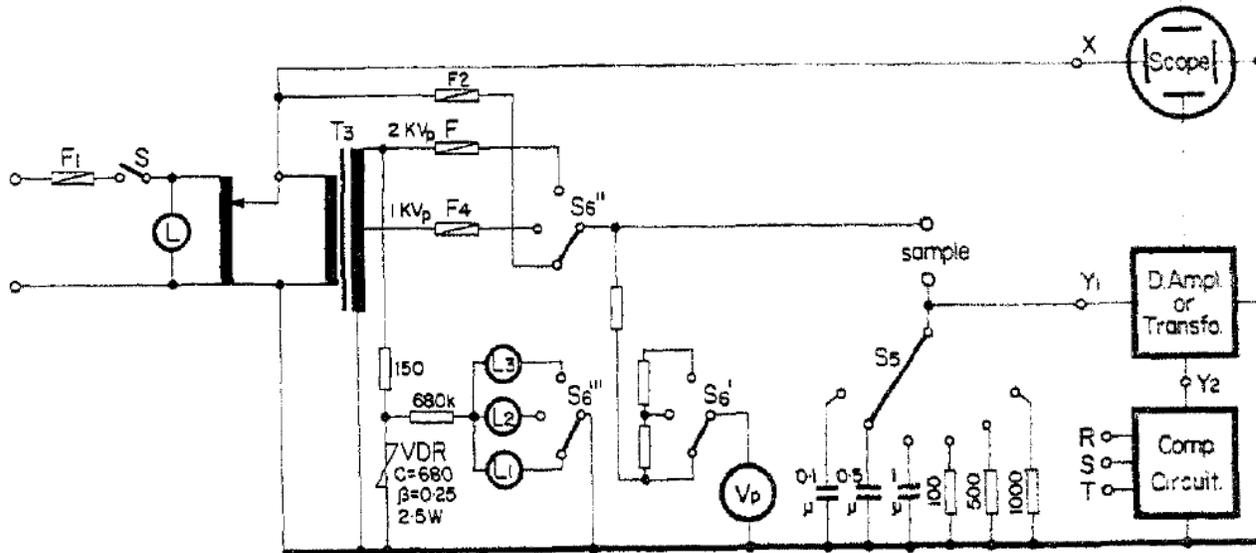
Compensate

3-phase supply-1962

The points **0**, **T**, **S**, **R** represent the neutral point and the **three** phases of a three phase **50** c/s line.

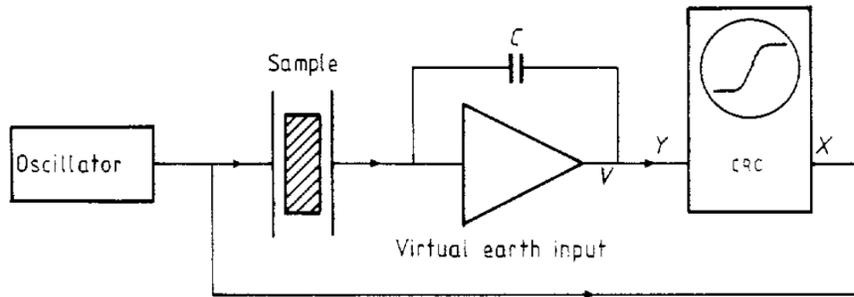


Across the two secondaries of the transformers **T1** and **Tz** we have two sinusoidal voltages 90° out of phase with each other. The two potentiometers **P1** and **Pz** and the two switches **S1** and **Sz** permit one to obtain any desired voltage. The connection between the two secondaries of the transformers is such that the two voltages add up so that the **sum** appears at point **Y2**. With switch **S3** a 10 : 1 voltage divider **can** be inserted.

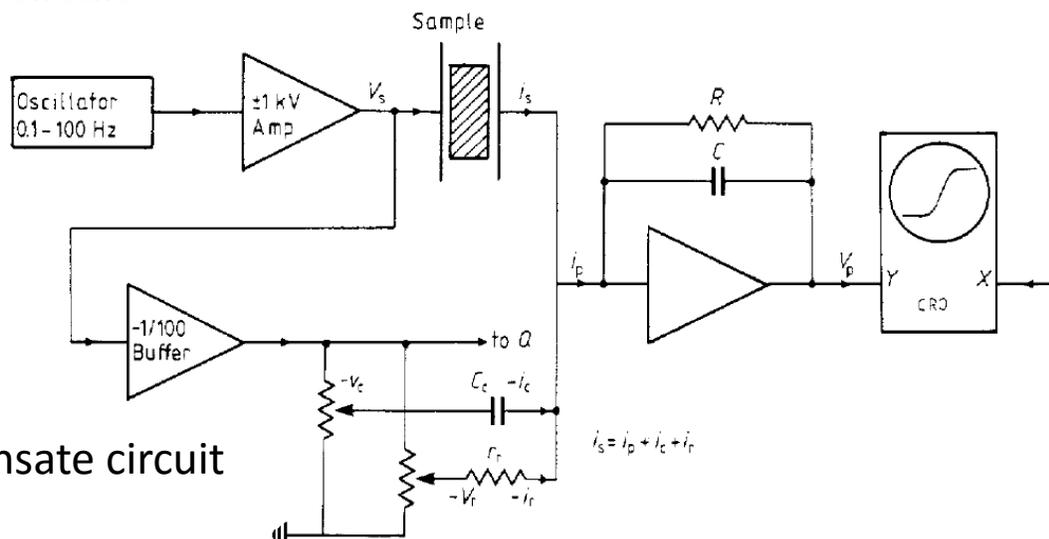


If the voltage applied to the ferroelectric sample has the same phase **as** **T**, then the switch **Sz** and the potentiometer **Pz** vary the ratio **a : b** of the two axes of the ellipses (**loss** compensation); the switch **S1** and the potentiometer **P1** vary the inclination of the major axis with respect to the **E** axis (linear capacitance compensation).

Current to charge converter-1983

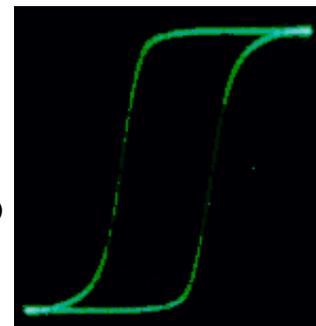
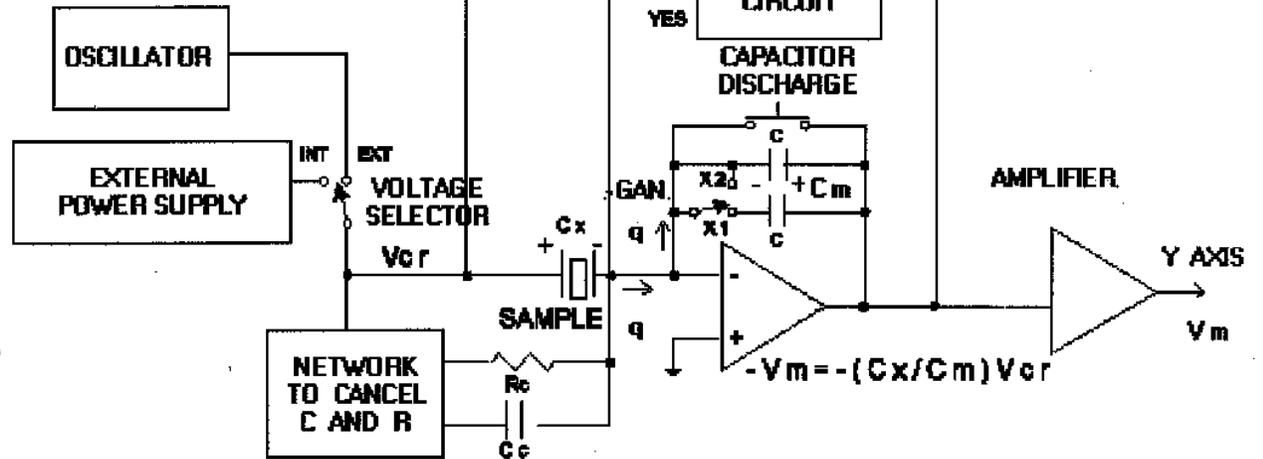
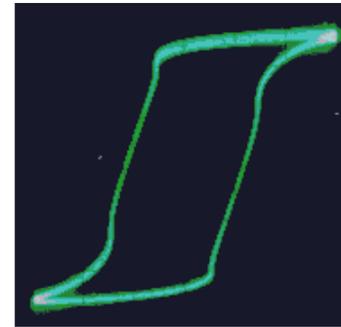


Basic circuit



Compensate circuit

Feedback capacitors C_c are switched from 10 to 1000 nF, and feedback resistors R are switched from 10 MR up to open circuit.



In our circuit R_c and C_c have values of 24.9 kohm and 4700 pF respectively. These values allow the compensation of the ferroelectric sample resistance and linear capacitance in the following ranges: $1245 \text{ kohm} \leq R_x \leq \infty$, and $0 \leq C_x \leq 94 \text{ pF}$.

PUND setup

